## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:		)	E' I1 D. IZ1
AKANE MASUMOTO, et al.		; )	Examiner: Joseph R. Kosack Group Art Unit: 1626
Application No.: 10/583,126		· )	Group Fart Clint. 1020
371(c) Date: June 16, 2006		· )	
For:	FIELD EFFECT TRANSISTOR AND METHOD OF PRODUCING SAME	; ; ;	September 16, 2008
P.O. Bo	ssioner for Patents ox 1450 dria, VA 22313-1450		

## RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the restriction requirement set forth in the Office Action dated August 27, 2008, Applicants provisionally elect to prosecute the Group I claims, namely Claims 1 to 4 and 7. The election is made without traverse.

Applicants' undersigned attorney may be reached in our Costa Mesa,

California office at (714) 540-8700. All correspondence should continue to be directed to our below-listed address.

Respectfully submitted,

/Damond E. Vadnais/
Damond E. Vadnais
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